PLUS Search Results for S/N 10081840, Searched August 02, 2004

The Patent Linguistics Utility System (PLUS) is a USPTO automated search system for U.S. Patents from 1971 to the present. PLUS is a query-by-example search system which produces a list of patents that are most closely related linguistically to the application searched. This search was prepared by the staff of the Scientific and Technical Information Center, SIRA.

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5369771
6038671
5509139
5664225
5857116
5963738
5754798
6314471
5644755
5832299
5790895
6421754
5684997
6112273
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5822600 71 5920728 71 5219742 67 5219742 67 5909696 64 5864705 64 6055643 63 5437039 62 5606713 62 5864656 62 6094700 61 6000043 61 6360327 61	
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6112273 49 6049852 48

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Most Frequently Occurring Classifications of Patents Returned From A Search of 10081840 on August 02, 2004

Original Classifications 7 710/260 4 713/300 3 710/49 2 710/269 2 713/323 2 713/340 2 714/10

Cross-Reference Classifications

- 5 713/324
- 4 710/260
- 4 713/300
- 4 713/310
- 4 713/320
- 4 714/24
- 3 710/261
- 3 710/266
- 3 710/3
- 3 713/323
- 3 713/340
- 2 710/100
- 2 710/18
- 2 710/262
- 2 710/263
- 2 710/264
- 2 710/269
- 2 711/142
- 2 712/229
- 2 712/43
- 2 713/100
- 2 713/330
- 2 714/22

Combined Classifications

- 11 710/260
 - 8 713/300
- 5 713/320
- 5 713/323
- 5 713/324
- 5 713/340
- 4 710/261
- 4 710/269
- 4 710/49

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- 713/310 4
- 4 714/24
- 710/266 3
- 3 710/3
- 3 713/100
- 3 713/330
- 2 710/100
- 2 710/15
- 710/18
- 2 710/262
- 2 710/263
- 2 710/264
- 710/67
- 2 711/142
- 711/144 2
- 2 711/145
- 2 712/229
- 2 712/43
- 2 713/1
- 713/322
- 2 714/10
- 714/22
- 714/34

10081840_CLSTITLES Titles of Most Frequently Occurring Classifications of Patents Returne d

From A Search of 10081840 on August 02, 2004

11	710/260 Class 710/260	710 :	, 4 XR) ELECTRICAL COMPUTERS AND DIGITAL DATA PROCESSING SYSTEMS: INPUT/OUTPUT INTERRUPT PROCESSING
8	713/300 Class 713/300	713 :	ELECTRICAL COMPUTERS AND DIGITAL PROCESSING SYSTEMS: SUPPORT COMPUTER POWER CONTROL
5	713/320 Class 713/300 713/320	713 :	R, 4 XR) ELECTRICAL COMPUTERS AND DIGITAL PROCESSING SYSTEMS: SUPPORT COMPUTER POWER CONTROL .Power conservation
5		713 :	ELECTRICAL COMPUTERS AND DIGITAL PROCESSING SYSTEMS: SUPPORT COMPUTER POWER CONTROL .Power conservationActive/idle mode processing
5	712/20	713 : 0	R, 5 XR) ELECTRICAL COMPUTERS AND DIGITAL PROCESSING SYSTEMS: SUPPORT COMPUTER POWER CONTROL .Power conservationBy shutdown of only part of system
5	Class 713/30	713 :	SYSTEMS: SUPPORT COMPUTER POWER CONTROL
4	710/261 Class 710/26 710/26	710 : 50	OR, 3 XR) : ELECTRICAL COMPUTERS AND DIGITAL DATA PROCESSING SYSTEMS: INPUT/OUTPUT INTERRUPT PROCESSING .Multimode interrupt processing
4	710/269	(2 (OR, 2 XR)

	710/260		10081840_CLSTITLES : ELECTRICAL COMPUTERS AND DIGITAL DATA PROCESSING SYSTEMS: INPUT/OUTPUT INTERRUPT PROCESSING .Handling vector
4	710/1	710	: ELECTRICAL COMPUTERS AND DIGITAL DATA PROCESSING SYSTEMS: INPUT/OUTPUT INPUT/OUTPUT DATA PROCESSING .Input/Output access regulationInput/Output interrupting
4	713/300	713	OR, 4 XR) : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING SYSTEMS: SUPPORT COMPUTER POWER CONTROL .By external command
4		714	OR, 4 XR) : ERROR DETECTION/CORRECTION AND FAULT DETECTION/RECOVERY DATA PROCESSING SYSTEM ERROR OR FAULT HANDLING
	714/1 714/2 714/24		<pre>.Reliability and availabilityFault recoverySafe shutdown</pre>
3	710/260	710	OR, 3 XR) : ELECTRICAL COMPUTERS AND DIGITAL DATA PROCESSING SYSTEMS: INPUT/OUTPUT INTERRUPT PROCESSING .Programmable interrupt processing
3	710/3 Class 710/1 710/3	(0 710	OR, 3 XR) : ELECTRICAL COMPUTERS AND DIGITAL DATA PROCESSING SYSTEMS: INPUT/OUTPUT INPUT/OUTPUT DATA PROCESSING .Input/Output addressing
3	713/100 Class 713/100	713	OR, 2 XR) : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING SYSTEMS: SUPPORT RECONFIGURATION (E.G., CHANGING SYSTEM SETTING

3 713/330 (1 OR, 2 XR)

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	Class 713: 713/300 713/330	10081840_CLSTITLES ELECTRICAL COMPUTERS AND DIGITAL PROCESSING SYSTEMS: SUPPORT COMPUTER POWER CONTROL .Power sequencing
2	710/100 (0 Class 710 :	OR, 2 XR) ELECTRICAL COMPUTERS AND DIGITAL DATA PROCESSING SYSTEMS: INPUT/OUTPUT INTRASYSTEM CONNECTION (E.G., BUS AND BUS TRANSACTION PROCESSING)
2	710/15 (1 Class 710 710/1 710/15	OR, 1 XR) : ELECTRICAL COMPUTERS AND DIGITAL DATA PROCESSING SYSTEMS: INPUT/OUTPUT INPUT/OUTPUT DATA PROCESSING .Peripheral monitoring
2	710/18 (0 Class 710 710/1 710/15 710/18	OR, 2 XR) : ELECTRICAL COMPUTERS AND DIGITAL DATA PROCESSING SYSTEMS: INPUT/OUTPUT INPUT/OUTPUT DATA PROCESSING .Peripheral monitoring Activity monitoring
2	710/262 (0 Class 710 710/260 710/262	OR, 2 XR) : ELECTRICAL COMPUTERS AND DIGITAL DATA PROCESSING SYSTEMS: INPUT/OUTPUT INTERRUPT PROCESSING .Interrupt inhibiting or masking
2	710/263 (0 Class 710 710/260 710/263	OR, 2 XR) : ELECTRICAL COMPUTERS AND DIGITAL DATA PROCESSING SYSTEMS: INPUT/OUTPUT INTERRUPT PROCESSING .Interrupt queuing
2	710/264 (0 Class 710 710/260 710/264	OR, 2 XR) : ELECTRICAL COMPUTERS AND DIGITAL DATA PROCESSING SYSTEMS: INPUT/OUTPUT INTERRUPT PROCESSING .Interrupt prioritizing
2		OR, 1 XR) : ELECTRICAL COMPUTERS AND DIGITAL DATA PROCESSING SYSTEMS: INPUT/OUTPUT INPUT/OUTPUT DATA PROCESSING .Peripheral adapting Input/Output data modification

		710/67		10081840_CLSTITLESKeystroke interpretation
2	711/	142 Class	(0 711	OR, 2 XR) : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING SYSTEMS: MEMORY
		711/100 711/117 711/118 711/141 711/142		STORAGE ACCESSING AND CONTROL .Hierarchical memoriesCachingCoherencyWrite-through
2	711/	144 Class	(1 711	OR, 1 XR) : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING SYSTEMS: MEMORY
		711/100 711/117 711/118 711/141		STORAGE ACCESSING AND CONTROL .Hierarchical memoriesCachingCoherency
		711/144		Cache status data bit
2	711/	145 Class		OR, 1 XR) : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING SYSTEMS: MEMORY
		711/100 711/117 711/118 711/141 711/145		STORAGE ACCESSING AND CONTROL .Hierarchical memoriesCachingCoherencyAccess control bit
•	710	/11/143		OD 2 VP
2	112,	Class		: ELECTRICAL COMPUTERS AND DIGITAL PROCESSING SYSTEMS: PROCESSING ARCHITECTURES AND INS
TRUC	CTION			PROCESSING
		712/220 712/229		PROCESSING CONTROL .Mode switch or change
2	712	/43 Class		OOR, 2 XR) 2: ELECTRICAL COMPUTERS AND DIGITAL PROCESSING SYSTEMS: PROCESSING ARCHITECTURES AND INS
TRU	CTION			PROCESSING
		712/1 712/32		PROCESSING ARCHITECTURE .Microprocessor or multichip or multimodule processor having sequential program contr
ol		712/42 712/43		OperationMode switching

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2 713/1 (1 OR, 1 XR) Class 713: ELECTRICAL COMPUTERS AND DIGITAL PROCESSING SYSTEMS: SUPPORT 713/1 DATA PROCESSING SYSTEM INITIALIZATION OR CONFIGURATION (E.G., INITIALIZING, SET	N
P, CONFIGURATION, OR RESETTING)	
2 713/322 (1 OR, 1 XR) Class 713: ELECTRICAL COMPUTERS AND DIGITAL PROCESSING SYSTEMS: SUPPORT COMPUTER POWER CONTROL .Power conservationBy clock speed control (e.g., clock on/of	
2 714/10 (2 OR, 0 XR) Class 714: ERROR DETECTION/CORRECTION AND FAULT DETECTION/RECOVERY 714/100 DATA PROCESSING SYSTEM ERROR OR FAULT HANDL	ING
714/1 .Reliability and availability 714/2Fault recovery 714/3By masking or reconfiguration 714/10Of processor	
2 714/22 (0 OR, 2 XR) Class 714: ERROR DETECTION/CORRECTION AND FAULT DETECTION/RECOVERY 714/100 DATA PROCESSING SYSTEM ERROR OR FAULT HANDI	JING
714/1 .Reliability and availability 714/2Fault recovery 714/15State recovery (i.e., process or data fi	ile)
714/22With power supply status monitoring	
2 714/34 (1 OR, 1 XR) Class 714: ERROR DETECTION/CORRECTION AND FAULT DETECTION/RECOVERY 714/100 DATA PROCESSING SYSTEM ERROR OR FAULT HAND	LING
714/1 .Reliability and availability 714/25Fault locating (i.e., diagnosis or testi	ng)
714/32Particular stimulus creation 714/34Halt, clock, or interrupt signal (e.g.	,

10081840_CLSTITLES freezing, hardware breakpoint, single-stepp

ing)